

# MOS FIELD EFFECT TRANSISTOR 2SK1658

# N-CHANNEL MOS FET FOR SWITCHING

#### **DESCRIPTION**

The 2SK1658 is an N -channel vertical type MOS FET which can be driven by 2.5 V power supply.

As the MOS FET is low Gate Leakage Current, it is suitable for appliances including Filter Circuit.

#### **FEATURES**

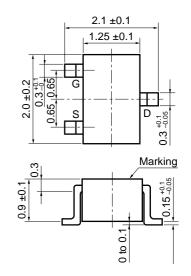
- Directly driven by ICs having a 3 V power supply.
- Has low Gate Leakage Current
   IGSS = ±5 nA MAX. (VGS = ±3.0 V)

# ABSOLUTE MAXIMUM RATINGS (TA = 25°C)

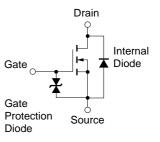
Drain to Source Voltage (Vss = 0 V)	Voss	30	V
Gate to Source Voltage (V <sub>DS</sub> = 0 V)	Vgss	±7	V
Drain Current (DC) (Tc = 25°C)	$I_{D(DC)}$	±100	mΑ
Drain Current (pulse) Note	I <sub>D(pulse)</sub>	±200	mΑ
Total Power Dissipation (T <sub>A</sub> = 25°C)	Рт	150	mW
Channel Temperature	Tch	150	°C
Operating Temperature	$T_{opt}$	-55 to +80	°C
Storage Temperature	Tstg	-55 to +150	°C

**Note.** PW  $\leq$  10 ms, Duty Cycle  $\leq$  50%

#### PACKAGE DRAWING (Unit: mm)



#### **EQUIVALENT CIRCUIT**



**Remark** The diode connected between the gate and source of the transistor serves as a protector against ESD. When this device is actually used, an additional protection circuit is externally required if a voltage exceeding the rated voltage may be applied to this device.

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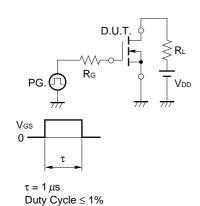
Not all devices/types available in every country. Please check with local NEC representative for availability and additional information.

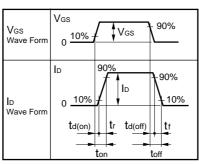


# **★ ELECTRICAL CHARACTERISTICS (TA = 25°C)**

CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Zero Gate Voltage Drain Current	Inss	V <sub>DS</sub> = 30 V, V <sub>GS</sub> = 0 V			10	μΑ
Gate Leakage Current	lgss	Vgs = ±3.0 V, Vps = 0 V			±5.0	nA
Gate Cut-off Voltage	V <sub>GS(off)</sub>	$V_{DS} = 3.0 \text{ V}, \text{ ID} = 1.0 \ \mu\text{A}$	0.9	1.2	1.5	V
Forward Transfer Admittance	<b>y</b> fs	V <sub>DS</sub> = 3.0 V, I <sub>D</sub> = 10 mA	20	40		mS
Drain to Source On-state Resistance	RDS(on)1	Vgs = 2.5 V, lb = 10 mA		25	45	Ω
	RDS(on)2	Vgs = 4.0 V, lb = 10 mA		18	25	Ω
Input Capacitance	Ciss	Vps = 3.0 V		15		pF
Output Capacitance	Coss	Vgs = 0 V		10		pF
Reverse Transfer Capacitance	Crss	f = 1 MHz		1.5		pF
Turn-on Delay Time	td(on)	V <sub>DD</sub> = 3.0 V, I <sub>D</sub> = 10 mA		50		ns
Rise Time	<b>t</b> r	Vgs = 3.0 V		23		ns
Turn-off Delay Time	td(off)	R <sub>G</sub> = 10 Ω		34		ns
Fall Time	tf	R <sub>L</sub> = 300 Ω		43		ns

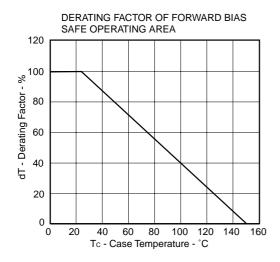
## TEST CIRCUIT SWITCHING TIME

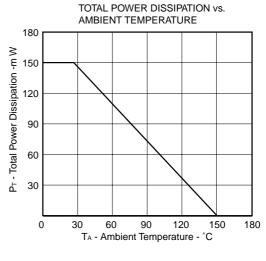


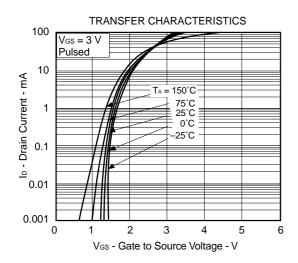


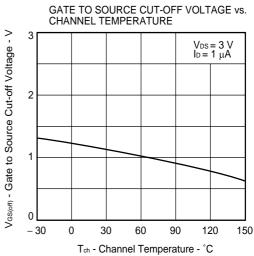


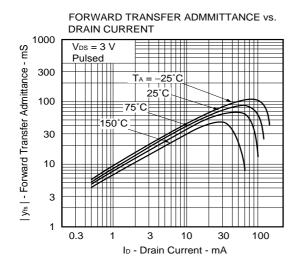
## TYPICAL CHARACTERISTICS (TA = 25°C)

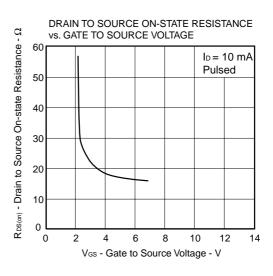


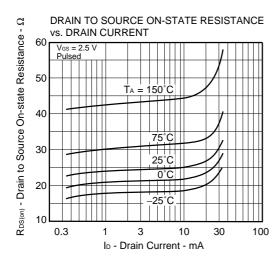


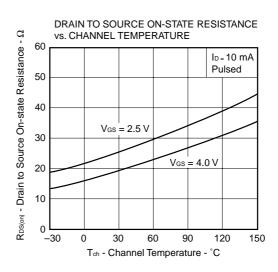


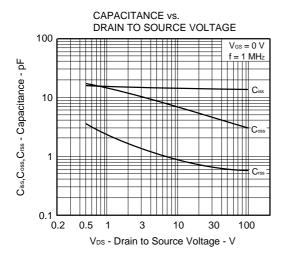


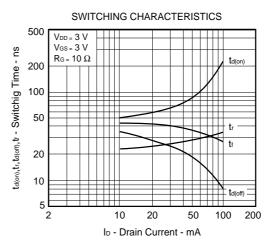


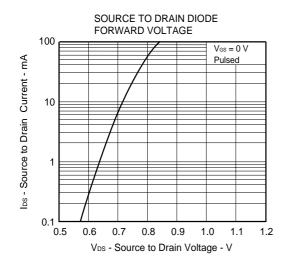














# RECOMMENDED SOLDERING CONDITIONS

Recommended solder conditions for this product are described below.

For details on recommended soldering conditions, refer to Information Document "Semiconductor Device Mounting Technology Manual" (C10535E).

For soldering methods and conditions other than those recommended, consult NEC.

## **Surface Mount Type**

#### 2SK1658

Soldering Method	Soldering Conditions	Symbol of Recommended Conditions
Infrared reflow	Package peak temperature: 235°C, Time: 30 seconds MAX. (210°C MIN.), Number of times: 3 MAX.	IR35-00-3
VPS	Package peak temperature: 215°C, Time: 40 seconds MAX. (200°C MIN.), Number of times: 3 MAX.	VP15-00-3
Wave soldering	Soldering bath temperature: 260°C MAX., Time: 10 seconds MAX., Number of times: 1, Preheating temperature: 120°C MAX. (package surface temperature)	WS60-00-1

Caution Do not use two or more soldering methods in combination.

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